

10/723211

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**FACSIMILE COVER SHEET****Certificate****AUG 17 2005****of Correction**

**DATE:** August 11, 2005  
**FILE NO:** INFN/SZ0030  
**TO:** Certificate of Correction Branch  
Commissioner for Patents  
**FAX NO:** 1-571-273-8300  
**FROM:** Randol W. Read  
**PAGE(S) with cover:** 3  
**ORIGINAL TO FOLLOW?** ☐ YES ☒ NO

**RE:**  
**PATENT NO.:** US 6,888,767 B1  
**ISSUE DATE:** May 3, 2005  
**TITLE:** DUAL POWER SENSING SCHEME FOR A MEMORY  
DEVICE  
**U.S. SERIAL NO.:** 10/723,211  
**FILING DATE:** November 26, 2003  
**INVENTOR(S):** Jonghee Han  
**EXAMINER:** Son T. Dinh  
**GROUP ART UNIT:** 2818  
**CONFIRMATION NO.:** 8517

Attached are the following document(s) for the above-referenced application / issued patent:

1. Request for Certificate of Correction
2. Certificate of Correction (form SB/44)

**CONFIDENTIALITY NOTE**

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**AUG 17 2005**

**RECEIVED  
CENTRAL FAX CENTER****AUG 11 2005**PATENT  
SZS&Z Ref. No. : IO031112PUS  
Atty. Dkt. No. INFN/SZ0030**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Jonghee Han

Serial No.: 10/723,211

Filed: November 26, 2003

For: DUAL POWER SENSING  
SCHEME FOR A  
MEMORY DEVICE§  
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Group Art Unit: 2818

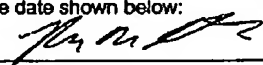
Examiner: Son T. Dinh

Patent No.: US 6,888,767 B1

Issued: May 3, 2005

Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**CERTIFICATE OF MAILING OR TRANSMISSION**

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August 11, 2005  
Date  
Randol W. Read

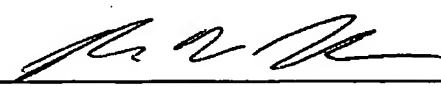
Dear Sir:

**REQUEST FOR CERTIFICATE OF CORRECTION**

Attached is a Certificate of Correction for correcting several errors throughout the printed patent.

Applicant submits that the errors mentioned above were not by the applicant, but were made during the printing of the patent.

Respectfully submitted,

  
Randol W. Read  
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AUG 17 2005

PTO/SB/44 (04-05)

Approved for use through 04/30/2007. OMB 0651-0033

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(Also Form PTO-1050)

**UNITED STATES PATENT AND TRADEMARK OFFICE**  
**CERTIFICATE OF CORRECTION**

PATENT NO.: US 6,888,767 B1

Page 1 of 1

APPLICATION NO.: 10/723,211

ISSUE DATE: May 3, 2005

INVENTOR(S): Jonghee Han

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 1, Lines 61-62, replace "VH\_BLSA1/VL\_BLSA<sub>1</sub> and VH\_BLSA2N/L\_BLSA2" with --VH\_BLSA1/VL\_BLSA1 and VH\_BLSA2/VL\_BLSA2--.

In Column 2, Line 28, replace "VL\_BLSA1" with --VL\_BLSA1--; Line 31, replace "VINT\_M" with --V<sub>INT\_M</sub>--; Line 32, replace "V<sub>INT</sub>M" with --V<sub>INT\_M</sub>--.

In Column 4, Line 41, replace "BLSAS" with --BLSAs--.

In Column 5, Line 34, "ΦACTIVE<sub>2</sub>" with --Φ<sub>ACTIVE2</sub>--; Line 61, replace "switches <sup>222</sup>" with --switches 222--.

In Column 6, Line 3, replace "VINT" with --V<sub>INT</sub>--; Line 7, replace "VINT—<sub>M1</sub> and VINT—<sub>M2</sub>" with --V<sub>INT—M1</sub> and V<sub>INT—M2</sub>--; Line 13, replace "VINT—<sub>M1</sub> and V<sub>INT</sub> M2" with --V<sub>INT—M2</sub>--; Line 26, replace "ΦPRE CHARGE" with --Φ<sub>PRE\_CHARGE</sub>--.

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PAGE 3/3 \* RCVD AT 8/11/2005 7:27:27 PM [Eastern Daylight Time] \* SVR:USPTO-EFXRF-6/24 \* DNIS:2738300 \* CSID:7136234846 \* DURATION (mm-ss):00-58

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